



PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1

of

4

**Complete if Known**

Application Number

10/043,362

Filing Date

January 9, 2002

First Named Inventor

Warren M. Farnworth

Group Art Unit

2827

Examiner Name

D. Zarneke

Attorney Docket Number

4212.1US (99-0306.01/US)

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
022		US-4,769,345	09/06/88	Butt et al.	
		US-4,821,151	04/11/89	Pryor et al.	
		US-4,866,571	09/12/89	Butt	
		US-4,967,260	10/30/90	Butt	
		US-5,014,159	05/07/91	Butt	
		US-5,323,051	06/21/94	Adams et al.	
		US-6,251,488	06/26/01	Miller et al.	
		US- 6,259,962	07/10/01	Gothait	
		US- 6,268,584	07/31/01	Keicher et al.	
		US- 6,337,122	01/08/02	Grigg et al.	
		US- 6,391,251	05/21/02	Keicher et al.	
		US- 6,482,576	11/19/02	Farnworth et al.	
		US- 6,489,007	12/03/02	Grigg et al.	
		US- 6,524,346	02/25/03	Farnworth	
		US- 6,544,821	04/08/03	Akram	
		US- 6,544,902	04/08/03	Farnworth	
		US- 6,549,821	04/15/03	Farnworth et al.	
		US- 6,562,278	05/13/03	Farnworth et al.	
		US- 6,585,927	07/01/03	Grigg et al.	
		US- 6,635,333	10/21/03	Grigg et al.	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
Examiner Signature			Date Considered			

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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)  Sheet 2 of 4	<b>Complete if Known</b>	
	Application Number	10/043,362
	Filing Date	January 9, 2002
	First Named Inventor	Warren M. Farnworth
	Group Art Unit	2827
	Examiner Name	D. Zameke
	Attorney Docket Number	4212 IUS (99-0306 01/IUS)

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DAZ		U.S. Patent Application Publication No. 2002/0098623 A1 to Akram, dated July 25, 2002	
		U.S. Patent Application Publication No. 2002/0171177 A1 to Kritchman et al., dated November 21, 2002	
		U.S. Patent Application Publication No. 2003/0003180 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication No. 2003/0003380 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication No. 2003/0003405 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication No. 2003/0043360 A1 to Farnworth, dated March 6, 2003	
		U.S. Patent Application Publication No. 2003/0068584 A1 to Farnworth et al., dated April 10, 2003	
		U.S. Patent Application Publication No. 2003/0072926 A1 to Grigg et al., dated April 17, 2003	
		U.S. Patent Application Publication No. 2003/0077418 A1 to Grigg et al., dated April 24, 2003	
		U.S. Patent Application Publication No. 2003/0089999 A1 to Akram, dated May 15, 2003	
DAZ		U.S. Patent Application Publication No. 2003/0092220 A1 to Akram, dated May 15, 2003	

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	Application Number	10/043,362
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	First Named Inventor	Warren M. Farnworth
	Group Art Unit	2827
	Examiner Name	D. Zarneke
	Attorney Docket Number	4212 IUS (99-0306 01/IUS)

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DF		U.S. Patent Application Publication No. 2003/0093173 A1 to Farnworth et al., dated May 15, 2003	
		U.S. Patent Application Publication No. 2003/0102566 A1 to Farnworth, dated June 5, 2003	
		U.S. Patent Application Publication No. 2003/0151167 A1 to Kritchman et al., dated August 14, 2003	
		U.S. Patent Application Publication No. 2003/0201531 A1 to Farnworth et al., dated October 30, 2003	
		U.S. Patent Application Publication No. 2003/0203158 A1 to Farnworth et al., dated October 30, 2003	
		U.S. Patent Application Publication No. 2003/0207213 A1 to Farnworth, dated November 6, 2003	
DF		MILLER et al., "Maskless Mesoscale Materials Deposition", Deposition Technology, September 2001, pages 20-22	
DF		MILLER, "New Laser-Directed Deposition Technology", Microelectronic Fabrication, August 2001, page 16	
DF		Webpage, Objet Prototyping the Future, Objet FullCure700 Series, 1 page	
DF		Webpage, Objet Prototyping the Future, How it Works, 2 pages	
DF		U.S. Patent Application No. 09/589,841, filed June 8, 2000, entitled "Stereolithographic Methods for Forming a Protective Layer on a Semiconductor Device Substrate and Substrates Including Protective Layers So-Formed", inventor Farnworth et al.	

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	Examiner Name	D. Zarneke
	Attorney Docket Number	4212 IIS (99-0306 01/IIS)

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DA		U.S. Patent Application No. 09/651,930, filed August 31, 2000, entitled "Semiconductor Device Including Leads in Communication with Contact Pads Thereof and a Stereolithographically Fabricated Package Substantially Encapsulating the Leads and Methods for Fabricating the Same", inventor Salman Akram	
DA		U.S. Patent Application No. 10/370,755, filed February 20, 2003, entitled "Chip Scale Package Structures and Method of Forming Conductive Bumps Thereon", inventor Warren M. Farnworth	
DA		U.S. Patent Application No. 10/608,749, filed June 26, 2003, entitled "Methods for Labeling Semiconductor Device Components", inventor Grigg et al.	
DA		U.S. Patent Application No. 10/672,098, filed September 26, 2003, entitled "Apparatus and Methods for Use in Stereolithographic Processing of Components and Assemblies", inventor Warren M. Farnworth	
DA		U.S. Patent Application No. 10/690,417, filed October 20, 2003, entitled "Methods of Coating and Singulating Wafers and Chip-Scale Packages Formed Therefrom", inventor Farnworth et al.	

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